## **Power MOSFET**

# 30 V, 130 A, Single N-Channel, SO-8FL

#### **Features**

- Low R<sub>DS(on)</sub> to Minimize Conduction Losses
- Low Capacitance to Minimize Driver Losses
- Optimized Gate Charge to Minimize Switching Losses
- These are Pb-Free Devices\*

#### **Applications**

- Refer to Application Note AND8195/D
- CPU Power Delivery
- DC-DC Converters
- Low Side Switching

## MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise stated)

Parameter		Symbol	Value	Unit	
Drain-to-Source Voltage			$V_{DSS}$	30	V
Gate-to-Source Vo	Gate-to-Source Voltage		$V_{GS}$	±20	V
Continuous Drain		$T_A = 25^{\circ}C$	Ι <sub>D</sub>	21	Α
Current R <sub>θJA</sub> (Note 1)		T <sub>A</sub> = 85°C		15	
Power Dissipation R <sub>θJA</sub> (Note 1)		T <sub>A</sub> = 25°C	$P_{D}$	2.31	W
Continuous Drain		$T_A = 25^{\circ}C$ ID		13	Α
Current R <sub>θJA</sub> (Note 2)	Steady State	T <sub>A</sub> = 85°C		9.5	
Power Dissipation R <sub>θJA</sub> (Note 2)	State	T <sub>A</sub> = 25°C	$P_{D}$	0.9	W
Continuous Drain		T <sub>C</sub> = 25°C	I <sub>D</sub>	130	Α
Current R <sub>θJC</sub> (Note 1)		T <sub>C</sub> = 85°C		93	
Power Dissipation R <sub>θJC</sub> (Note 1)		T <sub>C</sub> = 25°C	$P_{D}$	86.2	W
Pulsed Drain Current	T <sub>A</sub> = 25°C, t <sub>p</sub> = 10 μs		I <sub>DM</sub>	260	Α
Operating Junction and Storage Temperature		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	
Source Current (Body Diode)		I <sub>S</sub>	71	Α	
Drain to Source DV/DT		dV/dt	6	V/ns	
Single Pulse Drain-to-Source Avalanche Energy ( $T_J$ = 25°C, $V_{DD}$ = 30 V, $V_{GS}$ = 10 V, $I_L$ = 32 $A_{pk}$ , $L$ = 1.0 mH, $R_G$ = 25 $\Omega$ )		EAS	512	mJ	
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)		TL	260	°C	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

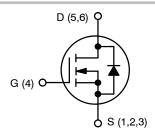
- 1. Surface-mounted on FR4 board using 1 sq-in pad, 1 oz Cu.
- 2. Surface-mounted on FR4 board using the minimum recommended pad size.



## ON Semiconductor®

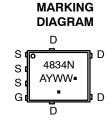
#### http://onsemi.com

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
	3.0 m $\Omega$ @ 10 V	
30 V	4.0 mΩ @ 4.5 V	130 A



**N-CHANNEL MOSFET** 





A = Assembly Location

Y = Year WW = Work Week ■ = Pb-Free Package

(Note: Microdot may be in either location)

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NTMFS4834NT1G	SO-8FL (Pb-Free)	1500 Tape / Reel
NTMFS4834NT3G	SO-8FL (Pb-Free)	5000 Tape / Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case (Drain)	$R_{ hetaJC}$	1.45	
Junction-to-Ambient - Steady State (Note 3)	$R_{ hetaJA}$	54	°C/W
Junction-to-Ambient - Steady State (Note)	$R_{ hetaJA}$	138.7	

## **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS				•	•		-
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		30			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /				21		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V,	T <sub>J</sub> = 25 °C			1	
		V <sub>DS</sub> = 24 V				10	μΑ
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS}$	= ±20 V			±100	nA
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}, I_D =$	= 250 μA	1.5		2.5	V
Negative Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>				6.1		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	11.5 V	I <sub>D</sub> = 30 A		2.6	3.0	mΩ
			I <sub>D</sub> = 15 A		2.5		
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 30 A		3.5	4.0	
			I <sub>D</sub> = 15 A		3.4		
Forward Transconductance	9FS	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 15 A			35.2		S
CHARGES, CAPACITANCES & GATE RESIS	TANCE				•		
Input Capacitance	C <sub>ISS</sub>				4500		
Output Capacitance	C <sub>OSS</sub>	V <sub>GS</sub> = 0 V, f = 1 MHz, V <sub>DS</sub> = 12 V			960		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>				500		
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 4.5 V, V <sub>DS</sub> = 15 V; I <sub>D</sub> = 30 A			32	48	nC
Threshold Gate Charge	Q <sub>G(TH)</sub>				5.4		
Gate-to-Source Charge	$Q_{GS}$				12		
Gate-to-Drain Charge	$Q_{GD}$				11		
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 11.5 V, V <sub>DS</sub> = 15 V; I <sub>D</sub> = 30 A			74		nC
SWITCHING CHARACTERISTICS (Note 6)							
Turn-On Delay Time	t <sub>d(ON)</sub>				20		
Rise Time	t <sub>r</sub>	$V_{GS}$ = 4.5 V, $V_{DS}$ = 15 V, $I_{D}$ = 15 A, $R_{G}$ = 3.0 $\Omega$			34		- ns
Turn-Off Delay Time	t <sub>d(OFF)</sub>				22		
Fall Time	t <sub>f</sub>				23		
Turn-On Delay Time	t <sub>d(ON)</sub>	$V_{GS}$ = 11.5 V, $V_{DS}$ = 15 V, $I_{D}$ = 15 A, $R_{G}$ = 3.0 $\Omega$			11		ns
Rise Time	t <sub>r</sub>				23		
Turn-Off Delay Time	t <sub>d(OFF)</sub>				37		
Fall Time	t <sub>f</sub>				15		

Surface-mounted on FR4 board using 1 sq-in pad, 1 oz Cu.
 Surface-mounted on FR4 board using the minimum recommended pad size.

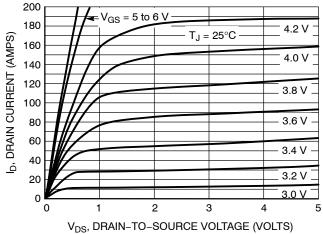
<sup>5.</sup> Pulse Test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2%.
6. Switching characteristics are independent of operating junction temperatures.

## **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit		
DRAIN-SOURCE DIODE CHARACTERISTICS									
Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V,	T <sub>J</sub> = 25°C		0.77	1.2	.,		
		I <sub>S</sub> = 30 A	T <sub>J</sub> = 125°C		0.70		V		
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = 0 \text{ V, dIS/dt} = 100 \text{ A/}\mu\text{s,}$ $I_{S} = 30 \text{ A}$			34		ns		
Charge Time	t <sub>a</sub>				18				
Discharge Time	t <sub>b</sub>				16				
Reverse Recovery Charge	Q <sub>RR</sub>				25.9		nC		
PACKAGE PARASITIC VALUES									
Source Inductance	L <sub>S</sub>	T <sub>A</sub> = 25°C			0.65		nΗ		
Drain Inductance	L <sub>D</sub>				0.005		nΗ		
Gate Inductance	L <sub>G</sub>				1.84		nΗ		
Gate Resistance	R <sub>G</sub>				1.4		Ω		

<sup>5.</sup> Pulse Test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2%.
6. Switching characteristics are independent of operating junction temperatures.

## **TYPICAL PERFORMANCE CURVES**



/<sub>DS</sub>, DRAIN-TO-SOURCE VOLTAGE (VOLTS)
Figure 1. On-Region Characteristics

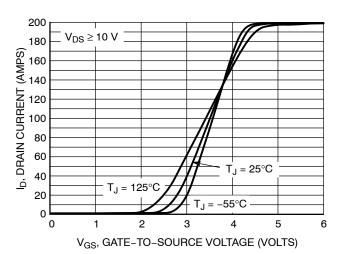


Figure 2. Transfer Characteristics

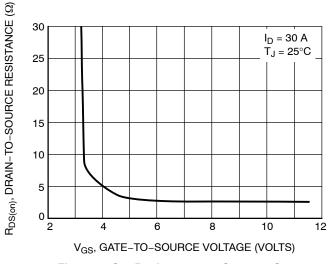


Figure 3. On-Resistance vs. Gate-to-Source Voltage

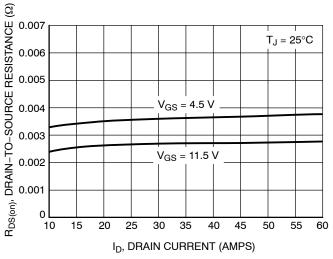


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

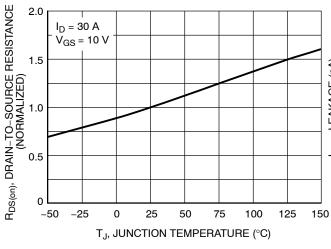


Figure 5. On–Resistance Variation with Temperature

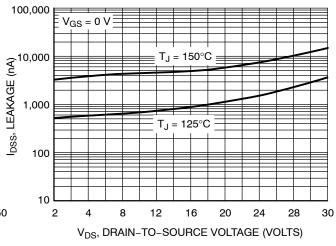
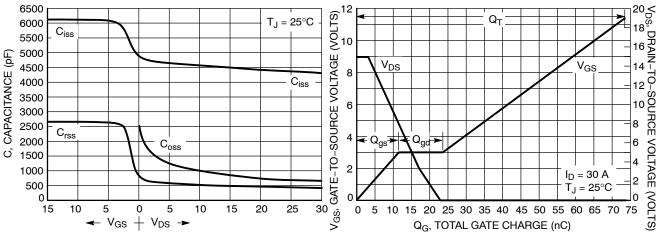


Figure 6. Drain-to-Source Leakage Current vs. Voltage

## **TYPICAL PERFORMANCE CURVES**



GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Figure 7. Capacitance Variation

Figure 8. Gate-To-Source and Drain-To-Source Voltage vs. Total Charge

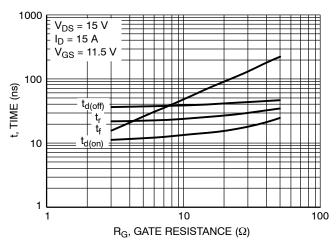


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

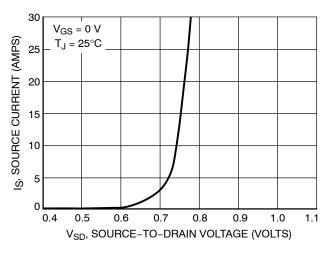


Figure 10. Diode Forward Voltage vs. Current

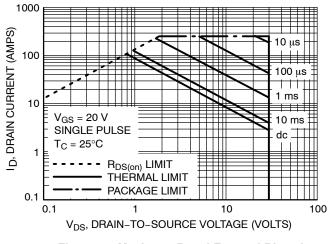


Figure 11. Maximum Rated Forward Biased Safe Operating Area

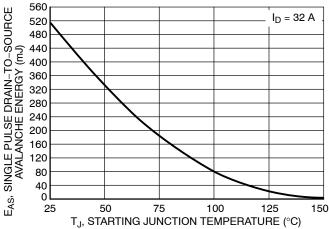
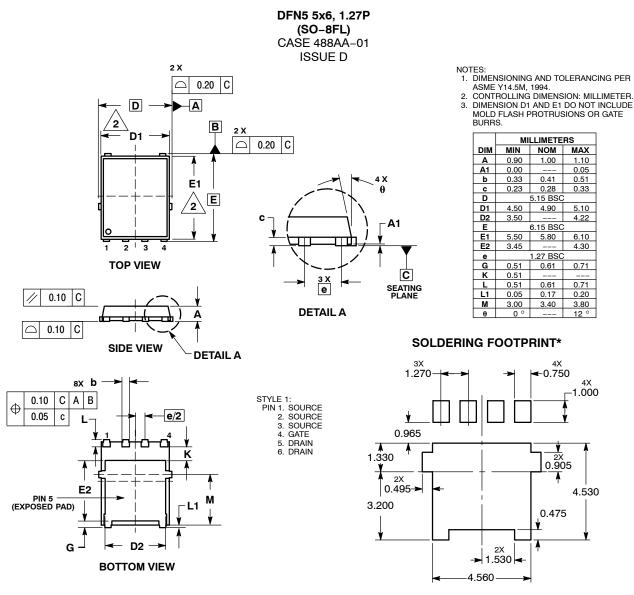


Figure 12. Maximum Avalanche Energy vs. Starting Junction Temperature

## PACKAGE DIMENSIONS



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and IIII are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81–3–5773–3850 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative